

L Number	Hits	Search Text	DB	Time stamp
1	11515	(stage multistage multi-stage multiple-stage) near2 (divider counter)	USPAT	2003/02/07 15:35
3	20	2.clm.	USPAT	2003/02/07 15:36
2	199	((stage multistage multi-stage multiple-stage) near2 (divider counter)) with (mux multiplex?r)	USPAT	2003/02/07 16:43
4	5	4193539.URPN.	USPAT	2003/02/07 15:39
5	3907	(cascad\$3 chain\$3) near2 (divider counter)	USPAT	2003/02/07 15:41
6	53	((cascad\$3 chain\$3) near2 (divider counter)) with (mux multiplex?r)	USPAT	2003/02/07 15:43
7	1	"4327328"	USPAT	2003/02/07 15:43
8	2	("4064423"   "4199276").PN.	USPAT	2003/02/07 15:44
10	426	(timing timer) with (wakeup wake-up)	USPAT	2003/02/07 17:04
11	20	10.ab.	USPAT	2003/02/07 16:48
16	317	period with (wakeup wake-up)	USPAT	2003/02/07 17:34
17	19	16.ab.	USPAT	2003/02/07 17:32
24	43	16.clm.	USPAT	2003/02/07 17:32
25	1078	circuit\$2 with (wakeup wake-up wak\$3 adj up)	USPAT	2003/02/07 17:38
26	666	circuit\$2 near3 (wakeup wake-up wak\$3 adj up)	USPAT	2003/02/07 17:35
27	49	26.ab.	USPAT	2003/02/07 17:35
28	22	(circuit\$2 with (wakeup wake-up wak\$3 adj up)) with (programmable adjustable configurable)	USPAT	2003/02/07 17:38
-	7	1.clm.	USPAT; US-PGPUB	2003/02/06 16:29
-	75	(wakeup wake-up) with programmable	USPAT; US-PGPUB	2003/02/06 16:35
-	16	(wakeup wake-up) with configurable	USPAT; US-PGPUB	2003/02/06 16:36
-	6	(wakeup wake-up) with adjustable	USPAT; US-PGPUB	2003/02/06 16:37
-	2707	programmable near2 delay	USPAT; US-PGPUB	2003/02/06 16:39
-	104	6.ti.	USPAT; US-PGPUB	2003/02/06 16:54
-	146	(programmable near2 delay) with (mux multiplex?r)	USPAT; US-PGPUB	2003/02/06 16:55
-	6	8.ab.	USPAT; US-PGPUB	2003/02/06 16:59
-	31	8.clm.	USPAT; US-PGPUB	2003/02/06 17:06
-	1	((programmable near2 delay) with (mux multiplex?r)) with divid\$3	USPAT; US-PGPUB	2003/02/06 17:09
-	8554	delay with (mux multiplex\$3)	USPAT; US-PGPUB	2003/02/06 17:24
-	250	(delay with (mux multiplex\$3)) with divid\$3	USPAT; US-PGPUB	2003/02/06 17:10
-	55	13.clm.	USPAT; US-PGPUB	2003/02/06 17:23
-	374	delay with (mux multiplex\$3)	USOCR	2003/02/06 17:24
-	0	16.ab.	USOCR	2003/02/06 17:24
-	17	(delay with (mux multiplex\$3)) with divid\$3	USOCR	2003/02/06 17:29
3050		divider with select\$3	USOCR	2003/02/06 17:30
-	75	(divider with select\$3) with delay	USOCR	2003/02/06 17:35
-	0	23.ab.	USOCR	2003/02/06 17:36
-	371	(mux multiplex\$3) with divid\$3	USOCR	2003/02/06 17:37
-	2	(wakeup wake-up) with circuit\$2	USOCR	2003/02/06 17:38
-	3	(wakeup wake-up) with circuit\$2	IBM_TDB	2003/02/06 17:38
-	0	(wakeup wake-up) with (mux multiplex\$3)	IBM_TDB	2003/02/06 17:40
-	2	(wakeup wake-up) same (mux multiplex\$3)	IBM_TDB	2003/02/06 17:38
-	4	(wakeup wake-up) with timer	IBM_TDB	2003/02/06 17:41
-	11	(wakeup wake-up) same timer	IBM_TDB	2003/02/06 17:41